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## PART B - FEE(S) TRANSMITTAL

er with applicable fee(s), to: Mail Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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XILINX, INC ATTN: LEGAL DEPARTMENT 2100 LOGIC DR **SAN JOSE, CA 95124** 

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(1		2006/	1,	August

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
4		Tim Tuan	X-1462 US	8241
10/666,669	09/19/2003			* BOWED

TITLE OF INVENTION: DISABLING UNUSED/INACTIVE RESOURCES IN PROGRAMMABLE LOGIC DEVICES FOR STATIC PO REDUCTION

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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inventor(6):

Tim Tuan et al.

Assignee: XILINX, INC.

Serial No.: 10/666,669

conf.no. 8241

Filed:

September 19, 2003

Title: Disabling Unused/Inactive Resources in Programmable Logic Devices for Static Power

Reduction

Docket No.:

X-1462 US

Enclosed: Return Receipt Postcard

Part B. - Fee(s) Transmittal

Date: August 1, 2006

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